1   25. [Amended] A system, comprising:

2   a memory array; and

3   a memory array controller comprising

4   a negative charge pump; and

5   a block controller coupled to the negative charge pump, comprising:

6   a negative level shifter to switch an output between a read-mode

7   voltage and an erase-mode voltage dependent upon a

8   selection signal input;

9   a positive voltage switch coupled to the negative level [shift circuit]

10   shifter;

11   a bit line driver coupled to the positive voltage switch and coupled

12   to said memory array; and

13   a word line driver coupled to said positive voltage switch, coupled

14   to the negative level shifter, and coupled to said memory
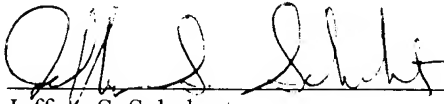
15   array.

CONCLUSION

For the aforementioned reasons, Applicant submits that claims 25-51 should be entered and are in condition for allowance and that the amendment to claim 25 is appropriate and is in condition for allowance. Indication of the same is respectfully requested. If Examiner has any questions concerning this application, Applicant requests Examiner to telephone the undersigned at the telephone number shown below.

Please charge any additional fees or credit any overpayment to Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

Feb. 19, 2002

Date:

Jeffrey S. Schubert
Reg. No. 43,098
12400 Wilshire Blvd, Seventh Floor
Los Angeles, California 90025-1026
(512) 330-0844